## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert	S. Weiner		
Title: Non-volat	ile transistor memory array incorporat	ing read-only elements with	single mask set
Docket No.: Filed: Examiner: Customer No.:	2800.450US1 March 26, 2004 Thinh Nguyen 76287	Serial No.: Due Date: Group Art Unit: Confirmation No.:	10/810,035 March 4, 2010 2818 8458
Mail Stop Amend Commissioner for P.O. Box 1450 Alexandria, VA 2	Patents		
We are transmitting	ng herewith the following attached iter	ms (as indicated with an "X"	):
<ul> <li>X Response to Ex Parte Quayle Action (2 pgs.)</li> <li>X Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), Copies of Cited References (1).</li> <li>X Authorization to charge Deposit Account 19-0743 in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 37 C.F.R. § 1.97(c).</li> </ul>			
Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.			
SCHWEGMAN, L Customer No.: 76	<u>UNDBERG &amp; WOESSNER, P.A.</u> 287	By: Robert E. Mates Reg. No. 35,271	1REWI
electronic filing syste	DER 37 CFR 1.8: The undersigned hereby cerem EFS-Web, and is addressed to: Mail Stop 23-1450 on this <u>4</u> day of March, 2010.		

Nicole Jack

Name